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PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10045416	FILING DATE 10/26/2001	CLASS 327	SUBCLASS 713	GAU 2816	EXAMINER LE
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\*\*APPLICANTS: Kunst David; Vinn Charles;

not ASD

\*\*CONTINUING DATA VERIFIED:

None ASD 10/27/03

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\*\* FOREIGN APPLICATIONS VERIFIED:

None ASD 10/27/03

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		ATTORNEY DOCKET NO	
35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		A-70429/ENB	
Verified and Acknowledged Examiners's initials ASD			
TITLE : Method and apparatus for optimizing the accuracy of an electronic circuit			
U.S. DEPT. OF COMM./PAT. & TM.-PTO-430L (Rev. 12-94)			

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
			Print Fig.
TERMINAL DISCLAIMER		Application Examiner	
PREPARED FOR ISSUE		WARNING: The information disclosed herein may be restricted by the United States Code Title 35, Sections 122, 181 and 368. Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.	

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